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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,554	06/20/2003	Gupta Abhay	J&R-1062	6081

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EXAMINER

MYERS, PAUL R

ART UNIT PAPER NUMBER

2112

DATE MAILED: 09/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/600,554

Applicant(s)

ABHAY ET AL.

Examiner

Paul R. Myers

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 June 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-28 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
- Paper No(s)/Mail Date 6/20/03, 8/12/03.

- 4) ☐ Interview Summary (PTO-413)
- Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "A11" has been used to designate A11, A12 and A13 in Figure 2. The examiner notes the specification refers to A11, A12 and A13. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

2. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the Read and Write timing must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must

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be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1-7, 9-11, 14-17, 19-20, 27-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hiller et al PN 5,081,575 in view of Khandekar et al PN 6,173,354.

In regards to claims 1 and 27-28: Hiller teaches a configuration comprising: a first device (PE1); a cross bar (6); a second device (PMEM1) connected to said first device (PE1) through said cross bar (6), said first device (PE1) accessing said second device (PMEM1) through said cross bar (6) to at least one of read data (PMEM1 output data figure 12) from said second device (PMEM1) and write data (PMEM1 input data) to said second device (PMEM1). Hiller does not teach the claimed read and write timing. Khandekar teaches the prior art read and write access cycle of figure 3. Khandekar teaches upon the occurrence of a read access (Column

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3 lines 30-61 read signal) to a second device, a first device reading the data (Data[63:0]) emitted from said second device upon receipt of a ready signal (DRDY) produces by said second device (the DRDY signal is produced by the device that places the data on the bus. In a read it is the slave in a write it is the master. It indicates both that the data is ready and that the data is valid Column 3 lines 30-61) and supplied to said first device; and upon occurrence of a write access (Column 3 lines 30-61 write signal) from said first device to said second device: said first device emitting the data to be written (Data[63:0]) to said second device upon receipt by said first device of a ready signal (TRDY) produces by said second device and supplied to said first device; and said second device reading said data emitted from said first device upon receipt by said second device of a data valid signal (DRDY Column 3 lines 30-61) produces by said first device. It would have been obvious to a person of ordinary skill in the art at the time of the invention to use the prior art timing of Khandekar in the system of Hiller because this is the timing of the P6 which is a standard and is used by many devices.

In regards to claim 2: Hiller teaches a first address bus (Figure 12 PE1 Address); a second address bus (PMEM1 Address); a first read data bus (PE1 Data in conjunction with Read1); a second read data bus (PMEM1 input data); a first write data bus (PE1 Data in conjunction with Write1); a second write data bus (PMEM1 output data); said first device (PE1) and said crossbar (6) being connected to one another through said first address bus (PE1 address), said read data bus (PE1 Data) and said first write data bus (PE1 Data); and said second device (PMEM1) and said crossbar (6) being connected to one another through said second address bus (PMEM1 address), said second read data bus (PMEM1 input data), and said second write data bus (PMEM1 output data).

In regards to claim 3: Khandekar teaches the first device sends a read request when it wishes to make a read access.

In regards to claims 4 and 15: Khandekar teaches the first device sends an address at the same time as the request signal specifying a device and a point within said device from which data should be read.

In regards to claim 5: Hiller and Khandekar teach a plurality of second devices.

In regards to claims 6 and 16: Hiller and Khandekar teach addresses being places on an address bus.

In regards to claims 7 and 17: Khandekar teaches a grant signal (ACK).

In regards to claims 9 and 19: Khandekar teaches passing at least a portion of the address to the second device. The examiner notes that PEIsteering Vector is clearly a part of the address that would not be passed.

In regards to claim 10: Khandekar teaches the device passing the data stored at the address supplied.

In regards to claim 11: Khandekar teaches the DRDY signal is asserted at the same time that the data is provided.

In regards to claim 14: Khandekar teaches the first device sends a Write request when it wishes to make a Write access.

In regards to claim 20: Khandekar teaches the second device emits a ready signal (TRDY) when it is ready to receive data.

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5. Claims 8, 12-13, 18 and 21-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hiller et al PN 5,081,575 in view of Khandekar et al PN 6,173,354 as applied to claim 7 above, and further in view of Hanawa et al PN 5,375,215.

In regards to claims 8 and 18: Khandekar teaches a grant signal as described above. Khandekar does not expressly teach that the grant signal is included as part of the address bus. Hanawa expressly teaches an Address bus (170) that includes grant lines (220) as part of the address bus. It would have been obvious to a person of ordinary skill in the art at the time of the invention to include the grant signal on the address bus because this would have allowed for an easy method of grouping the signal lines.

In regards to claims 12-13, 21-22, 25-26: Khandekar teaches a Ready signal (Both Target Ready TRDY and Data Ready DRDY) as described above. Khandekar does not expressly teach that the Ready signal is included as part of the Data bus. Hanawa expressly teaches a bus (170) that includes additional control signals (210-220,240-250) as part of the bus. It would have been obvious to a person of ordinary skill in the art at the time of the invention to include the Ready signal on the Data bus because this would have allowed for an easy method of grouping the signal lines.

In regards to claim 23: Both Khandekar and Hiller teach the first device emits the data to be written.

In regards to claim 24: Khandekar teaches the first device emits the data valid (DRDY) signal at the same time as the first device emits the data.

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Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul R. Myers whose telephone number is 571 272 3639. The examiner can normally be reached on Mon-Thur 6:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PRM
September 26, 2005



**PAUL R. MYERS
PRIMARY EXAMINER**